

Would you like to work with world-leading scientists on developing cutting-edge technology for securing embedded IT systems?

HENSOLDT and **Secure Elements** jointly created **HENSOLDT Cyber**, based in Munich, to address the large and growing challenges to the security of embedded IT and cyber-physical systems. Our approach is based on secure hardware designs, novel techniques for secure production, combined with mathematically proved software. This will result in a trustworthy base for highly-secure systems.

Secure Elements is a company of leading researchers who have created highly-innovative hardware and software approaches for making IT systems fundamentally secure.

We are looking for highly motivated and talented

IC Designer ASIC/FPGA (junior and senior level ID MUC-07-2018-17)

Tasks:

- Contribute to the design of a RISC-V based SoC
- Define the product specification
- Develop RTL and integrate 3rd party IP
- Develop a verification and validation plan
- Develop an FPGA prototype and a test suite
- Perform synthesis and support ASIC development activities
- Support SW development team in post-silicon bring-up and firmware development

Skills:

- Solid understanding of computer architecture
- Proficient in FPGA or ASIC design using Verilog or VHDL
- Experience with high level design tools (preferably Vivado)
- Experience with simulation tools (preferably ModelSim, Questa)
- Familiar with scripting languages and test automation
- Knowledge of SW development for test, firmware or drivers

Work Experience:

- M.Sc. in Electronic Engineering or equivalent for a junior profile
- 5+ years of relevant experience in the industry for a senior profile

A modern, open plan offices and a dynamic spirit team with many benefits and opportunities is waiting for you!

Apply now or contact us to find out more about this software developer opportunity.