

# Hardware Developer (m/f/x)

## SKILLS

- At least 3 years of experience in VHDL/Verilog development
- Experience of implementation on FPGA or ASIC
- Knowledge of RTL simulation tools, synthesis and timing analysis
- Knowledge of PCB development
- Experience in defining and implementing a test plan
- Scripting (Python, tcl, bash) and knowledge of C/C++ is a plus
- Highly motivated to create high-end security solutions
- M.Sc. or PhD in Information Technologies

## TASKS

- Supporting the ASIC development
- Select, analyze, integrate and test IP Cores for specified functionalities
- Documentation of the hardware details in order to support the software team
- Design PCBs
- Create test plans for complete ASICs
- Test and bring up of chips and PCBs